



Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Application Number	10/716,595
Filing Date	November 20, 2003
First Named Inventor	Michael Farmwald et al.
Art Unit	2827
Examiner Name	Tan Nguyen
Attorney Docket Number	57941.000062

Sheet 1 of 20

U.S. PATENT DOCUMENTS

*Examiner Initials	Cite No.	DOCUMENT NUMBER Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	1.	US 3,031,643	04-24-1962	Sheftelman	
	2.	US 3,068,405	12-11-1962	Glazer	
	3.	US 3,204,180	08-31-1965	Bray	
	4.	US 3,243,665	03-29-1966	Fayer	
	5.	US 3,348,141	10-17-1967	Jensen	
	6.	US 3,377,518	04-09-1968	Radcliffe	
	7.	US 3,562,432	02-09-1971	Gabbard	
	8.	US 3,585,500	06-15-1971	Grubel	
	9.	US 3,654,395	04-04-1972	Schmidt	
	10.	US 3,671,865	06-20-1972	Szumila	
	11.	US 3,713,025	01-23-1973	McNair	
	12.	US 3,760,261	01-18-1973	Collins	
	13.	US 3,801,981	04-02-1974	Alpers	
	14.	US 3,886,454	03-27-1975	Oakley	
	15.	US 3,906,159	09-16-1975	Lutz	
	16.	US 4,021,625	05-03-1977	Bradley	
	17.	US 4,047,201	09-06-1977	Kerllenevich	
	18.	US 4,053,950	10-11-1977	Bourke	
	19.	US 4,056,843	11-01-1977	Bishop	
	20.	US 4,063,080	12-13-1977	Eichelberger	
	21.	US 4,079,448	03-14-1978	N'Guyen	
	22.	US 4,117,267	09-26-1978	Haberle	
	23.	US 4,142,069	02-27-1979	Stover	
	24.	US 4,214,261	07-22-1980	Bazin	
	25.	US 4,250,352	02-10-1981	Workman, Sr.	

EXAMINER SIGNATURE

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	26.	US 4,255,814	03-10-1981	Osborn	
	27.	US 4,271,483	06-02-1981	Baldwin	
	28.	US 4,320,505	03-16-1982	Braun, Jr.	
	29.	US 4,335,426	06-15-1982	Maxwell	
	30.	US 4,337,463	06-29-1982	Vangen	
	31.	US 4,359,735	11-16-1982	Lewis	
	32.	US 4,360,827	11-23-1982	Braun	
	33.	US 4,365,297	12-21-1982	Grisham, Jr.	
	34.	US 4,411,007	10-18-1983	Rodman	
	35.	US- 4,458,357	07-03-1984	Weymouth, et al.	
	36.	US- 4,493,021	01-01-1985	Agrawal, et al.	
	37.	US 4,500,991	02-19-1985	Lubcke	
	38.	US 4,532,569	07-30-1985	Salcedo	
	39.	US 4,596,025	06-17-1986	Satoh	
	40.	US 4,612,594	09-16-1986	Yamaura	
	41.	US- 4,635,196	01-06-1987	Nakamura, et al.	
	42.	US- 4,683,555	07-28-1987	Pinkham	
	43.	US 4,696,019	09-22-1987	Tulpule	
	44.	US- 4,727,455	02-23-1988	Neidig, et al.	
	45.	US 4,727,475	02-23-1988	Kiremidjian	
	46.	US 4,740,923	04-26-1988	Kaneko, et al.	
	47.	US 4,758,993	07-19-1988	Takemae	
	48.	US 4,811,336	03-07-1989	Backhaus	
	49.	US 4,846,937	07-18-1989	Driscoll	
	50.	US 4,847,833	07-11-1989	Doering	

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	51.	US 5,001,626	03-19-1991	Kashiyama	
	52.	US 5,021,722	06-04-1991	Llerena, et al.	
	53.	US 5,040,153	08-13-1991	Fung et al.	
	54.	US 5,175,822	12-29-1992	Dixon, et al.	
	55.	US 5,243,703	09-07-1993	Farmwald	
	56.	US 5,257,892	11-02-1993	Branch	
	57.	US 5,319,755	06-07-1994	Farmwald, et al.	
	58.	US 5,335,206	08-02-1994	Kawamoto	
	59.	US 5,361,343	11-01-1994	Kosonocky, et al.	
	60.	US 5,408,129	04-18-1995	Farmwald, et al.	
	61.	US 5,455,923	10-03-1995	Kaplinsky	
	62.	US 5,473,575	12-05-1995	Farmwald, et al.	
	63.	US 5,498,990	03-12-1996	Leung	
	64.	US 5,499,385	03-12-1996	Farmwald, et al.	
	65.	US 5,524,249	06-04-1996	Simmons	
	66.	US 5,524,249	06-04-1996	Suboh	
	67.	US 5,537,354	07-16-1996	Mochizuki	
	68.	US 5,561,433	10-01-1996	Chaney	
	69.	US 5,586,332	12-17-1996	Jain	
	70.	US 5,594,937	01-14-1997	Stiles	
	71.	US 5,606,717	03-25-1997	Farmwald, et al.	
	72.	US 5,613,077	03-18-1997	Leung	
	73.	US 5,615,169	03-25-1997	Leung	
	74.	US 5,629,897	05-13-1997	Iwamoto	
	75.	US 5,636,173	06-03-1997	Schaefer	

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	76.	US 5,638,334	06-10-1997	Farmwald, et al.	
	77.	US 5,655,113	08-05-1997	Leung	
	78.	US 5,657,481	08-12-1997	Farmwald, et al.	
	79.	US 5,657,841	08-19-1997	Morvan	
	80.	US 5,673,398	09-30-1997	Takeda	
	81.	US 5,696,729	12-09-1997	Kitamura	
	82.	US 5,712,882	01-27-1998	Miller	
	83.	US 5,734,685	03-31-1998	Bedell	
	84.	US 5,737,587	04-07-1998	Leung	
	85.	US 5,742,194	04-21-1998	Saeki	
	86.	US 5,745,421	04-28-1998	Pham, et al.	
	87.	US 5,754,838	05-19-1998	Shibata	
	88.	US 5,768,213	06-16-1998	Jung	
	89.	US 5,787,041	07-28-1998	Hill, et al.	
	90.	US 5,790,839	08-04-1998	Luk	
	91.	US 5,809,263	09-15-1998	Farmwald, et al.	
	92.	US 5,815,462	09-29-1998	Konishi	
	93.	US 5,826,026	10-27-1998	Friedman	
	94.	US 5,841,580	11-24-1998	Farmwald, et al.	
	95.	US 5,841,707	11-24-1998	Cline, et al.	
	96.	US 5,841,715	11-24-1998	Farmwald, et al.	
	97.	US 5,847,999	12-08-1998	Kwon	
	98.	US 5,860,125	01-12-1999	Reents	
	99.	US 5,886,946	03-23-1999	Ooishi	
	100.	US 5,915,105	06-22-1999	Farmwald, et al.	

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	101.	US 5,917,760	06-29-1999	Millar	
	102.	US 5,928,343	07-27-1999	Farmwald, et al.	
	103.	US 5,930,197	07-27-1999	Ishibashi	
	104.	US 5,953,263	09-15-1999	Farmwald, et al.	
	105.	US 5,954,804	09-21-1999	Farmwald, et al.	
	106.	US 5,983,320	11-20-1999	Farmwald, et al.	
	107.	US 5,995,443	11-30-1999	Farmwald, et al.	
	108.	US 5,995,904	09-21-1999	Willen	
	109.	US 6,014,339	01-11-2000	Kobayashi	
	110.	US 6,032,215	02-29-2000	Farmwald, et al.	
	111.	US 6,035,365	03-07-2000	Farmwald, et al.	
	112.	US 6,044,426	02-28-2000	Farmwald, et al.	
	113.	US 6,049,846	04-11-2000	Farmwald, et al.	
	114.	US 6,067,592	05-23-2000	Farmwald, et al.	
	115.	US 6,070,222	05-20-2000	Farmwald, et al.	
	116.	US 6,085,284	07-04-2000	Farmwald, et al.	
	117.	US 6,088,774	07-11-2000	Gillingham	
	118.	US 6,101,152	08-08-2000	Farmwald, et al.	
	119.	US 6,128,696	10-03-2000	Farmwald, et al.	
	120.	US 6,128,700	10-03-2000	Hsu	
	121.	US 6,182,184	01-30-2001	Farmwald, et al.	
	122.	US 6,185,644	02-06-2001	Farmwald, et al.	
	123.	US 6,226,723	05-01-2001	Gustavson	
	124.	US 6,259,288	07-10-2001	Nishimura	
	125.	US 6,260,097	07-10-2001	Farmwald, et al.	

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	126.	US 6,266,285	07-24-2001	Farmwald, et al.	
	127.	US 6,272,577	08-07-2001	Leung	
	128.	US 6,304,937	10-16-2001	Farmwald, et al.	
	129.	US 6,314,051	11-06-2001	Farmwald, et al.	
	130.	US 6,324,120	11-27-2001	Farmwald, et al.	
	131.	US 6,378,020	04-23-2002	Farmwald, et al.	
	132.	US 6,415,339	07-02-2002	Farmwald, et al.	
	133.	US 6,442,644	08-27-2002	Gustavson	
	134.	US 6,452,863	09-17-2002	Farmwald, et al.	
	135.	US 6,598,171	07-22-2003	Farmwald, et al.	

FOREIGN PATENT DOCUMENTS								
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		Country Code Number-Kind Code (if known)					YES	NO
	136.	WO	91/16680	10-31-1991	Farmwald, et al.		<input checked="" type="checkbox"/>	<input type="checkbox"/>
	137.	JP	61-107453	05-26-1986	Sano		<input type="checkbox"/>	<input type="checkbox"/>
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OTHER DOCUMENTS - NON-PATENT LITERATURE DOCUMENTS				
*Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	TRANSLATION	
			YES	NO
	138.	Anceau, François, "A Synchronous Approach for Clocking VLSI Systems," IEEE Journal of Solid-State Circuit, Vol. SC-17, No. 1, Feb. 1982, pp. 51-56.	<input type="checkbox"/>	<input type="checkbox"/>
	139.	Bursky, "Memory-CPU Interface Speeds Up Data Transfer," Electronic Design, pp. 137-142, March 1992	<input type="checkbox"/>	<input type="checkbox"/>
	140.	Bursky, "Multibaked Architecture Provides Fast, Flexible and Redundant DRAMs," Electronic Design, pgs 41-41, August 22, 1994.	<input type="checkbox"/>	<input type="checkbox"/>
	141.	Carter, "Survey of Synchronization Technique for a TDMA Satellite-Switched System," IEEE Transaction on Communications, Vol. COM-28, No. 8, pp. 1291-1300, August 1980	<input type="checkbox"/>	<input type="checkbox"/>
	142.	Claim chart for claims 30-33, 35-36, and 38-41 of the 6,378,020 patent based on the JEDEC Standard as filed in Replacement Request for Inter Partes Reexamination Under 35 U.S.C. §§ 311-318 in Reexam of U.S. Patent 6,378,020 dated January 31, 2008.	<input type="checkbox"/>	<input type="checkbox"/>
	143.	Claim chart for claims 30-33, 35-36, and 38-41 of the 6,378,020 patent based on Park, with additional reference to the JEDEC Standard as filed in Replacement Request for Inter Partes Reexamination Under 35 U.S.C. §§ 311-318 in Reexam of U.S. Patent 6,378,020 dated January 31, 2008.	<input type="checkbox"/>	<input type="checkbox"/>
	144.	Claim chart for claims 30-33, 35-36, and 38-41 of the 6,378,020 patent based on the iAPX Manual, with additional reference to Inagaki, the iAPX Specification, and Gover as filed in Replacement Request for Inter Partes Reexamination Under 35 U.S.C. §§ 311-318 in Reexam of U.S. Patent 6,378,020 dated January 31, 2008.	<input type="checkbox"/>	<input type="checkbox"/>
	145.	Claim chart for claims 30-33, 35-36, and 38-41 of the 6,378,020 patent based on Budde, with additional reference to Inagaki and Grover as filed in Replacement Request for Inter Partes Reexamination Under 35 U.S.C. §§ 311-318 in Reexam of U.S. Patent 6,378,020 dated January 31, 2008.	<input type="checkbox"/>	<input type="checkbox"/>
	146.	Defendant Infineon Technologies AG's First Supplemental Responses to Plaintiff's Interrogatories Nos. 3, 5, and 6, dated Dec. 4, 2000.	<input type="checkbox"/>	<input type="checkbox"/>
	147.	Defendant Samsung's Preliminary Invalidity Contentions, dated May 18, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	148.	Designations of Deposition Testimony For Inclusion In Trial Record in Hynix v. Rambus, CV 00-20905 RMW (N.D. Cal.), filed Apr. 10, 2006.	<input type="checkbox"/>	<input type="checkbox"/>

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			YES	NO
	149.	Ellingson, C.E., et al., "Dissemination of System Time," IEEE Transactions on Communications, Vol. COM-21, No. 5, pgs. 605-624, 5 May 1973	<input type="checkbox"/>	<input type="checkbox"/>
	150.	Exhibit H (Infringement Chart for Rambus's USP 6,032,214) to Rambus's Final infringement Contentions dated December 15, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	151.	Exhibit L (Infringement Chart for Rambus's USP 6,067,592) to Rambus's Final infringement Contentions dated December 15, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	152.	Exhibit N (Infringement Chart for Rambus's USP 6,324,120) to Rambus's Final infringement Contentions dated December 15, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	153.	Exhibit O (Infringement Chart for Rambus's USP 6,378,020) to Rambus's Final infringement Contentions dated December 15, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	154.	Exhibit Q (Infringement Chart for Rambus's USP 6,452,863) to Rambus's Final infringement Contentions dated December 15, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	155.	File History of U.S. Patent No. 6,032,214 ("the '214 parent patent").	<input type="checkbox"/>	<input type="checkbox"/>
	156.	File History of U.S. Patent 6,049,846 ("the '846 parent patent") deposited on April 18, 1990.	<input type="checkbox"/>	<input type="checkbox"/>
	157.	File History of U.S. Patent No. 6,182,184.	<input type="checkbox"/>	<input type="checkbox"/>
	158.	File History of U.S. Patent No. 6,324,120.	<input type="checkbox"/>	<input type="checkbox"/>
	159.	File History of U.S. Patent No. 6,378,020 issued April 23, 2002.	<input type="checkbox"/>	<input type="checkbox"/>

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			YES	NO
	160.	Gary, et al., "PowerPC603, A Microprocessor for Portable Computers," IEEE Design & Test of Computers, 1994	<input type="checkbox"/>	<input type="checkbox"/>
	161.	Hartmann, et al., "Synchronization Techniques for Digital Networks," IEEE Journal on Selected Areas in Communications, Vol. SAC-4, No. 4, pp. 506-513, July 1986	<input type="checkbox"/>	<input type="checkbox"/>
	162.	Horowitz, et al., "PLL Design for a 500MB/s Interface," ISSCC 93: Session 10 – High Speed Communication and Interfaces, 1993	<input type="checkbox"/>	<input type="checkbox"/>
	163.	Hynix-I, Exhibit S to Rambus Inc.'s Final Infringement Contentions, Hynix Data Sheet for HY5DU28422T dated April 1, 2001.	<input type="checkbox"/>	<input type="checkbox"/>
	164.	Hynix's Final Invalidity Contentions dated January 28, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	165.	Hynix's Memorandum Of Points And Authorities in Motion Of Its Motion For A New Trial On Invalidity Due To Prior Art, dated May 16, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	166.	Hynix's Motion For Judgment As A Matter Of Law And Motion For New Trial Regarding Invalidity Of Rambus's Asserted Claims For Failure To Satisfy The Written Description Requirement Of 35 U.S.C. § 112, ¶ 1, dated May 5, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	167.	Hynix's Notice Of Motion And Motion For Summary Judgment Of Invalidity Under Rambus's Proposed Construction Of "Synchronous Memory Device," dated Jan. 12, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	168.	Hynix's Notice Of Motion And Motion For Summary Judgment Of Invalidity Of Claims Referring To "Precharge Information" Under Rambus's Proposed Construction, dated Jan. 12, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	169.	Hynix's Notice Of Motion And Motion For Summary Judgment Of Invalidity Of U.S. Patent No. 6,378,020 For Failure To Satisfy The Written Description Requirement Of 35 U.S.C. § 112, ¶ 1, dated Feb. 11, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	170.	Hynix's Notice Of Motion And Motion For Summary Judgment Of Invalidity Of U.S. Patent Nos. 6,378,020 and 5,915,105 Under 35 U.S.C. § § 102 And/Or 103, dated Feb. 11, 2005.	<input type="checkbox"/>	<input type="checkbox"/>

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*Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	TRANSLATION	
			YES	NO
	171.	Hynix's Notice Of Motion And Motion For Summary Judgment Of Non-Infringement Under Proposed Constructions Of "Block Size Information," dated Jan, 12, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	172.	Hynix's Notice Of Motion And Motion For Summary Judgment Of Non-Infringement Under Hynix's Proposed Construction Of "Second External Clock Signal" Or, In The Alternative, Invalidity Under Rambus's Proposed Construction, dated Jan. 12, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	173.	Hynix's Notice Of Motion And Motion For Summary Judgment Of Non-Infringement Under Hynix's Proposed Construction Of The Term "Device" Or, In The Alternative, Invalidity Under Rambus's Proposed Construction, dated Jan. 12, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	174.	Hynix's Notice Of Motion And Motion For Summary Judgment Of Non-Infringement Under Hynix's Proposed Construction Of "Operation Code" Or, In The Alternative, Invalidity Under Rambus's Proposed Construction, dated Jan. 12, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	175.	Hynix's Notice Of Motion And Motion For Summary Judgment Of Non-infringement Under The Doctrine Of Equivalents Of Patent Claims Including "Read Request," dated Feb. 11, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	176.	Hynix's Notice Pursuant to 35 U.S.C. § 282, dated Feb. 3, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	177.	Hynix's Preliminary Invalidity Contentions dated October 1, 2001.	<input type="checkbox"/>	<input type="checkbox"/>
	178.	Hynix's Renewed Motion For Judgment As A Matter Of Law And Motion For New Trial Regarding The Claim Limitation "Delayed Locked Loop," dated May 5, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	179.	Hynix's Renewed Motion For Judgment As A Matter Of Law And Motion For New Trial Regarding Claims Containing The "Access Time Register" Limitations, dated May 5, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	180.	Hynix's Renewed Motion For Judgment As A Matter Of Law And Motion For New Trial Regarding The "In Response To A Rising/Falling Edge" Limitation, dated May 5, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	181.	Hynix's Renewed Motion For Judgment As A Matter Of Law And Motion For The New Trial Regarding The "Read Request" Claim Limitation, dated May 5, 2006.	<input type="checkbox"/>	<input type="checkbox"/>

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number	10/716,595
		Filing Date	November 20, 2003
		First Named Inventor	Michael Farmwald et al.
		Art Unit	2827
		Examiner Name	Tan Nguyen
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			YES	NO
	182.	Hynix's Reply Memorandum In Support Of Motion And Motion For Summary Judgment Of Invalidity Of Claims Referring To "Precharge Information" Under Rambus's Proposed Construction, dated Mar. 3, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	183.	Hynix's Reply Memorandum in Support Of Motion For Summary Judgment Of Invalidity Under Rambus's Proposed Construction Of "Synchronous Memory Device," dated Mar. 3, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	184.	Hynix's Reply Memorandum In Support Of Motion For Summary Judgment Of Non-Infringement Under Proposed Constructions Of "Block Size Information," dated Mar. 3, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	185.	Hynix's Reply Memorandum In Support Of Motion For Summary Judgment Of Non-Infringement Under Hynix's Proposed Construction Of "Second External Clock Signal" Or, In The Alternative, Invalidity Under Rambus's Proposed Construction, dated Mar. 3, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	186.	Hynix's Reply Memorandum In Support Of Motion For Summary Judgment Of Non-Infringement Under Hynix's Proposed Construction Of The Term "Device" Or, In The Alternative, Invalidity Under Rambus's Proposed Construction, dated Mar. 3, 2004	<input type="checkbox"/>	<input type="checkbox"/>
	187.	Hynix's Reply Memorandum in Support Of Motion For Summary Judgment Of Non-Infringement Under Hynix's Proposed Construction Of "Operation Code" Or, In The Alternative, Invalidity Under Rambus's Proposed Construction, dated Mar. 3, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	188.	Hynix's Reply Memorandum In Support Of Motion For Summary Judgment Of Invalidity Of U.S. Patent Nos. 6,378,020 For Failure To Satisfy The written Description Requirement of 35 U.S.C. § 112, ¶ 1, dated Mar. 4, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	189.	Hynix's Reply Memorandum In Support Of Motion For Summary Judgment Of Non-Infringement Under The Doctrine Of Equivalents Of Patent Claims Including "Read Request," dated Mar. 4, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	190.	Hynix's Reply Memorandum in Support Of Motion For Summary Judgment Of Invalidity Of Patent Claims Including "Precharge Information" For Failure To Satisfy The Written Description Requirement Of 35 U.S.C. § 112, ¶ 1, dated Mar. 4, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	191.	Hynix's Reply Memorandum In Support Of Motion For Summary Judgment Of Invalidity Of U.S. Patent Nos. 6,378,020 and 5,915,105 Under 35 U.S.C. § 102 And/Or 103, dated Mar. 4, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	192.	Hynix's Reply Memorandum In Support Of Motion For Summary Judgment Of Invalidity Of Patent Claims Including "Delay Locked Loop" For Failure To Satisfy The Written Description Requirement Of 35 U.S.C. § 112, ¶ 1, dated Mar. 4, 2005.	<input type="checkbox"/>	<input type="checkbox"/>

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			YES	NO
	193.	Hynix's Reply Re: Motion For A New Trial On Invalidity Based On Prior Art, dated June 8, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	194.	Hynix's Reply Re: Renewed Motion For Judgment As A Matter Of Law And Motion For New Trial Regarding The Claim Limitation "Delayed Locked Loop," dated May 26, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	195.	Hynix's Reply Re: Renewed Motion For Judgment As A Matter Of Law And Motion For New Trial Regarding Claims Containing The "Access Time Register" Limitations, dated May 26, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	196.	Hynix's Reply Re: Renewed Motion For Judgment As A Matter Of Law And Motion For New Trial Regarding Invalidity Of Rambus's Asserted Claims For Failure To Satisfy The Written Description Requirement Of 35 U.S.C. § 112, ¶1, dated May 26, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	197.	Hynix's Reply Re: Renewed Motion For Judgment As A Matter Of Law And Motion For New Trial Regarding The "In Response To A Rising/Falling Edge" Limitation, dated May 26, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	198.	Hynix's Reply Re: Renewed Motion For Judgment As A Matter Of Law And Motion For New Trial Regarding The "Read Request" Claim Limitation, dated May 26, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	199.	Hynix's Response To Rambus's Supplemental Brief Re Construction of "Synchronous Memory Device" And Definition Of "Packet," dated Mar. 29, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	200.	Hynix's Responsive Claim Construction Brief, dated Nov. 10, 2003.	<input type="checkbox"/>	<input type="checkbox"/>
	201.	Hynix's Notice Of Motion And Motion For Summary Judgment Of Invalidity Of Patent Claims Including "Precharge Information" For Failure To Satisfy The Written Description Requirement Of 35 U.S.C. § 112, ¶ 1, dated Feb. 11, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	202.	Hynix's Notice Of Motion And Motion For Summary Judgment Of Invalidity Of Patent Claims Including "Delay Locked Loop" For Failure To Satisfy The Written Description Requirement Of 35 U.S.C. § 112, ¶ 1, dated Feb. 11, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	203.	Infineon Technologies AG's Amended Prior Art Notice Pursuant to 35 U.S.C. § 282, dated Mar. 9, 2001	<input type="checkbox"/>	<input type="checkbox"/>

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		Art Unit	2827
		Examiner Name	Tan Nguyen
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			YES	NO
	204.	Inter Partes Reexamination Communication and Transmittal of Communication to Third Party Requester Inter Partes Reexamination forwarding Order Granting Request for Inter Partes Reexamination in Control No. 95/001,008 (6,715,020) dated 12/2007	<input type="checkbox"/>	<input type="checkbox"/>
	205.	Knight, Tom et al., "Self Terminating Low Voltage Swing CMOS Output Driver," IEEE 1987 Custom Integrated Circuits Conference, July 1987, pp. 289-292.	<input type="checkbox"/>	<input type="checkbox"/>
	206.	Kopetz, et al., "Clock Synchronization in Distributed Real-Time Systems," IEEE Transactions on Computers, Vol. C-30, No. 8, pp. 933-940, August 1987.	<input type="checkbox"/>	<input type="checkbox"/>
	207.	Kushiya, et al., "A 500-Megabyte/s Data-Rate 4.5M DRAM," IEEE Journal of Solid State Circuits, Vol. 28, No. 4, pgs. 490-498, April 1993.	<input type="checkbox"/>	<input type="checkbox"/>
	208.	Lee, et al., "A 2.5v CMOS Delay-Locked Loop for an 18Mbit, 500 Megabyte/s DRAM," IEEE Journal of Solid-State Circuits, Vol. 29, No. 12, pg 1491-196, December 1994	<input type="checkbox"/>	<input type="checkbox"/>
	209.	Lee, et al., "A 2.5V Delay-Locked Loop for an 18Mbit, 500 Megabyte/s DRAM," Solid State Circuits Conference, Digest of Technical Papers, 41 st ISSCC International, February 1994	<input type="checkbox"/>	<input type="checkbox"/>
	210.	Lindsey, et al., "Mutual Synchronization of Two Oscillators," IEEE National Telesystems Conference, November 1982	<input type="checkbox"/>	<input type="checkbox"/>
	211.	Lindsey, et al., Network Synchronization by Means of a Returnable Timing System," IEEE Transactions on Communications, Vol. COM-26, No. 6, pp.892-896, June 1978	<input type="checkbox"/>	<input type="checkbox"/>
	212.	Lindsey, et al., Network Synchronization of Random Signals," IEEE Transactions on Communications, Vol. COM-28, No. 8 pp., 1260-1265, August 1980	<input type="checkbox"/>	<input type="checkbox"/>
	213.	LS1 Logic Preliminary Datasheet, "LR2010 Floating-Point Accelerator," Nov. 1988.	<input type="checkbox"/>	<input type="checkbox"/>
	214.	LSI Logic Preliminary Datasheet, "LR2000 High Performance RISC Microprocessor," Sept. 1998.	<input type="checkbox"/>	<input type="checkbox"/>

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		Art Unit	2827
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			YES	NO
	215.	Lundelius, et al., "A New Fault-Tolerant Algorithm for Clock Synchronization," ACM Proceedings of the 3 rd Annual ACM Symposium on Principles of Distributed Computing," June 1984	<input type="checkbox"/>	<input type="checkbox"/>
	216.	Micron Technology's Supplemental Response to Interrogatory No. 5, alleging invalidity of the '105 patent, invalidity of the '214 patent, and invalidity of the '195 patent, as cited and filed in an Information Disclosure Statement Under 37 CFR §§ 1.933 & 1.555 in Inter Partes Reexaminations of 6,426,916 (95/000,166), 6,324,129 (95/000,178) and 6,182,184 (95/000,183) dated April 9, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	217.	MOSAID Technologies, Synclink Feasibility, v1.1., Synclinc Consortium, April 1996	<input type="checkbox"/>	<input type="checkbox"/>
	218.	Murphy Charts: Murphy Chart 1 (In Response to Taylor Chart 1) re Budde, pp. 1-62 (Redacted Version); Murphy Chart 2 (In Response to Taylor Chart 2) re iAPX432, pp. 1-84 (Redacted Version); Murphy Chart 11 (In Response to Taylor Chart 11) re U.S. Patent Nos. 5,915,105; 6,034,918; and 6,426,916, pp. 1-9, as cited and filed in a Supplemental Information Disclosure Statement Under 37 CFR §§ 1.933 & 1.555 in Inter Partes Reexaminations of 6,426,916 (95/000,166), 6,324,129 (95/000,178) and 6,182,184 (95/000,183) dated May 2, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	219.	Notice of Allowance and Fee Due issued on Dec. 8, 2006, in application serial no. 10/716,596, including Notice of Allowability and Information Disclosure Statement dated December 11, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	220.	Ofek, "Integration of Voice Communication on a Synchronous Optical Hypergraph," IEEE IBM T.J. Watson Research Center, 1988	<input type="checkbox"/>	<input type="checkbox"/>
	221.	Ogiue, Katsumi et al., "13-ns, 500-mW, 64-kbit ECL RAM Using HI-BICMOS Technology," IEEE Journal of Solid-State Circuits, Vol. SC-21, No. 5, pp. 681-685, Oct. 1986.	<input type="checkbox"/>	<input type="checkbox"/>
	222.	Order Denying Hynix's Motion For Summary Judgment Of Invalidity Of Claims Referring To Precharge Information Under Rambus's Proposed Construction, filed Jan. 4, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	223.	Order Denying Hynix's Motion For Summary Judgment Of Invalidity Of U.S. Patent Nos. 6,378,020 For Failure To Satisfy The written Description Requirement of 35 U.S.C. § 112, ¶ 1, filed May 12, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	224.	Order Denying Hynix's Motion For Summary Judgment Of Invalidity Of Patent Claims Including "Precharge Information" For Failure To Satisfy The Written Description Requirement Of 35 U.S.C. § 112, ¶ 1, filed May 12, 2005.	<input type="checkbox"/>	<input type="checkbox"/>

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			YES	NO
	225.	Order Denying Hynix's Motion For Summary Judgment Of Invalidity Of U.S. Patent Nos. 6,378,020 and 5,915,105 Under 35 U.S.C. § 102 And/Or 103, dated Feb. 28, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	226.	Order Denying Hynix's Motion For Summary Judgment Of Invalidity Of Patent Claims Including "Delay Locked Loop" For Failure To Satisfy The Written Description Requirement Of 35 U.S.C. § 112, ¶ 1, filed May 12, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	227.	Order Denying Hynix's Motion For Summary Judgment Of Invalidity Under Rambus's Construction Of "Synchronous Memory Device," filed Jan. 4, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	228.	Order Denying Hynix's Motion For Summary Judgment Of Non-Infringement Under Proposed Construction Of "Block Size Information," filed Jan. 4, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	229.	Order Denying Hynix's Motion For Summary Judgment Of Non-Infringement Under Hynix's Proposed Construction Of The Term "Device," Or In The Alternative, Invalidity Under Rambus's Proposed Construction, filed Jan. 4, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	230.	Order Denying Hynix's Motion For Summary Judgment Of Non-Infringement Under Hynix's Proposed Construction Of "Operation Code" Or, In The Alternative, Invalidity Under Rambus's Proposed Construction, filed Jan. 4, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	231.	Order Denying Hynix's Motion For Summary Judgment Of Non-Infringement Under The Doctrine Of Equivalents Of Patent Claims Including "Read Request," filed May 12, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	232.	Order Granting Hynix's Motion For Summary Judgment Of Non-Infringement Under Hynix's Proposed Construction Of The Term "Second External Clock" And Denying Alternative Motion For Summary Judgment Of Invalidity Under Rambus's Proposed Construction, Jan. 5, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	233.	Pattini, et al., "A Synchronization Technique for On-Board Master Clock of a Regenerative TDMA Satellite Communications System," IEEE International Conference on Communications 1985, Vol. 2, pp. 999-1004, June 1985	<input type="checkbox"/>	<input type="checkbox"/>
	234.	Peterson, "System-Level Concerns Set Performance Gains," High Performance Systems, pp. 71-77, September 1989	<input type="checkbox"/>	<input type="checkbox"/>
	235.	Plaintiff Micron Technology, Inc.'s Identification of Prior Art Pursuant to 35 U.S.C. § 282, dated Sept. 28, 2001.	<input type="checkbox"/>	<input type="checkbox"/>

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			YES	NO
	236.	Plaintiffs' Supplemental Submission Regarding "Synchronous Memory Device" And "Packet," dated Mar. 26, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	237.	Rambus Data Sheet 8/9 dated March 1, 1996.	<input type="checkbox"/>	<input type="checkbox"/>
	238.	Rambus Data Sheet, 16/18 Mbit (2Mx 8/9) and 64/72 Mbit (8Mx8/9) Concurrent RDRAM dated 1997.	<input type="checkbox"/>	<input type="checkbox"/>
	239.	Rambus Inc. v. Hynix Semiconductor, Inc., et al., No. C 05 00334 RMW (N.D. Cal) ("Hynix-II"), Rambus Inc.'s Disclosure of Asserted Claims and Preliminary Infringement Contentions Pursuant to Local Patent Rules filed in Reexam of U.S. 6,378,020 dated February 23, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	240.	Rambus Inc. v. Hynix Semiconductor, Inc., et al., No. C 05 00334 RMW (N.D. Cal) ("Hynix-II"), Rambus Inc.'s Joint Claim Construction and Prehearing Statement Pursuant to Patent Local Rule 4-3, including Appendix A, Disputed Claim Terms filed in Reexam of U.S. 6,378,020 dated July 11, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	241.	Rambus Inc. v. Hynix Semiconductor, Inc., et al., No. C 05 00334 RMW (N.D. Cal) ("Hynix-II"), Rambus Inc.'s Opening Markman Brief filed in Reexam of U.S. 6,378,020 dated August 24, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	242.	Rambus Inc. v. Hynix Semiconductor, Inc., et al., No. C 05 00334 RMW (N.D. Cal) ("Hynix-II"), Defendant's Manufacturer's Responsive Brief on Claim Construction filed in Reexam of U.S. 6,378,020 dated September 14, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	243.	Rambus Inc. v. Hynix Semiconductor, Inc., et al., No. C 05 00334 RMW (N.D. Cal) ("Hynix-II"), Rambus Inc.'s Reply to Brief to Manufacturer's Responsive Brief on Claim Construction filed in Reexam of U.S. 6,378,020 dated September 28, 2007	<input type="checkbox"/>	<input type="checkbox"/>
	244.	Rambus Inc.'s Opposition To Hynix's Motion For A New Trial On Validity Over Prior Art, dated June 6, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	245.	Rambus Inc.'s Opposition To Hynix's Motion For Judgment As A Matter Of Law And Motion For New Trial Regarding Invalidity Of Rambus's Asserted Claims For Failure To Satisfy The Written Description Requirement Of 35 U.S.C. § 112, ¶ 1, dated May 19, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	246.	Rambus Inc.'s Opposition To Hynix's Motion For Summary Judgment Of Invalidity Of U.S. Patent Nos. 6,378,020 For Failure To Satisfy The written Description Requirement of 35 U.S.C. § 112, ¶ 1, dated Feb. 25, 2005.	<input type="checkbox"/>	<input type="checkbox"/>

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			YES	NO
	247.	Rambus Inc.'s Opposition To Hynix's Motion For Summary Judgment Of Non-infringement Under The Doctrine Of Equivalents Of Patent Claims Including "Read Request," dated Feb. 25, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	248.	Rambus Inc.'s Opposition To Hynix's Motion For Summary Judgment Of Invalidity Of Patent Claims Including "Precharge Information" For Failure To Satisfy The Written Description Requirement Of 35 U.S.C. § 112, ¶ 1, dated Feb. 25, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	249.	Rambus Inc.'s Opposition To Hynix's Motion For Summary Judgment Of Invalidity Of U.S. Patent Nos. 6,378,020 and 5,915,105 Under 35 U.S.C. § § 102 And/Or 103, dated Feb. 25, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	250.	Rambus Inc.'s Opposition To Hynix's Motion For Summary Judgment Of Invalidity Of Patent Claims Including "Delayed Locked Loop" For Failure To Satisfy The Written Description Requirement Of 35 U.S.C. § 112, ¶ 1, dated Feb. 25, 2005.	<input type="checkbox"/>	<input type="checkbox"/>
	251.	Rambus Inc.'s Opposition To Hynix's Renewed Motion For Judgment As A Matter Of Law And Motion For New Trial Regarding The Claim Limitation "Delayed Locked Loop," dated May 19, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	252.	Rambus Inc.'s Opposition To Hynix's Renewed Motion For Judgment As A Matter Of Law And Motion For New Trial Regarding Claims Containing The "Access Time Register" Limitations, dated May 19, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	253.	Rambus Inc.'s Opposition To Hynix's Renewed Motion For Judgment As A Matter Of Law And Motion For New Trial Regarding The "In Response To A Rising/Falling Edge" Limitation, dated May 19, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	254.	Rambus Inc.'s Opposition To Hynix's Renewed Motion For Judgment As A Matter Of Law And Motion For The New Trial Regarding The "Read Request" Claim Limitation filed on May 19, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	255.	Rambus Interface, "Rambus Interface Technical Description, Version 4.2 - Preliminary of January 20, 1992.	<input type="checkbox"/>	<input type="checkbox"/>
	256.	Rambus, Inc., "NEC 18Mb Rambus - DRAM Core Interface," Draft of August 9, 1992.	<input type="checkbox"/>	<input type="checkbox"/>
	257.	Rambus, Inc., "p5 18M RDRAM Test Modes," Draft of August 3, 1992.	<input type="checkbox"/>	<input type="checkbox"/>

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		First Named Inventor	Michael Farmwald et al.
		Art Unit	2827
		Examiner Name	Tan Nguyen
Sheet	18 of 20	Attorney Docket Number	57941.000062

OTHER DOCUMENTS - NON-PATENT LITERATURE DOCUMENTS				
*Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	TRANSLATION	
			YES	NO
	258.	Rambus RDRAM, RDRAM Design Guide, April 20, 1995.	<input type="checkbox"/>	<input type="checkbox"/>
	259.	Rambus's Opening Claim Construction Brief, dated Oct. 27, 2003.	<input type="checkbox"/>	<input type="checkbox"/>
	260.	Rambus's Opposition To Hynix's Motion For Summary Judgment Of Invalidity Under Rambus's Proposed Construction Of "Synchronous Memory Device," dated Feb. 11, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	261.	Rambus's Opposition To Hynix's Motion For Summary Judgment Of Invalidity Of Claims Referring To "Precharge information" Under Rambus's Proposed Construction, dated Feb. 11, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	262.	Rambus's Opposition to Hynix's Motion For Summary Judgment Of Non-Infringement Under Proposed Constructions Of "Block Size Information," dated Feb. 11, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	263.	Rambus's Opposition To Hynix's Motion For Summary Judgment Of Noninfringement Under Hynix's Proposed Construction Of "Second External Clock Signal" Or, in The Alternative, Invalidity Under Rambus's Proposed Construction, dated Feb. 11, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	264.	Rambus's Opposition To Hynix's Motion For Summary Judgment Of Noninfringement Under Hynix's Proposed Construction Of The Term "Device" Or, In The Alternative, Invalidity Under Rambus's Proposed Construction, dated Feb. 11, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	265.	Rambus's Opposition To Hynix's Motion For Summary Judgment Of Noninfringement Under Hynix's Proposed Construction Of "Operation Code" Or, In The Alternative, Invalidity Under Rambus's Proposed Construction, dated Feb. 11, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	266.	Rambus's Reply Claim Construction Brief, dated Feb. 3, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	267.	Rambus's Supplemental Brief Re Construction of "Synchronous Memory Device" And Definition Of "Packet," dated Mar. 26, 2004.	<input type="checkbox"/>	<input type="checkbox"/>
	268.	Replacement Request for Inter Partes Reexamination Under 35 U.S.C. §§ 311-318 in Reexam of U.S. Patent 6,378,020 dated January 31, 2008.	<input type="checkbox"/>	<input type="checkbox"/>

EXAMINER SIGNATURE	DATE CONSIDERED
<p><small>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small></p>	

INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number	10/716,595
		Filing Date	November 20, 2003
		First Named Inventor	Michael Farmwald et al.
		Art Unit	2827
		Examiner Name	Tan Nguyen
Sheet	19 of 20	Attorney Docket Number	57941.000062

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			YES	NO
	269.	Request for <i>Inter Partes</i> Reexamination under 35 U.S.C. §§ 311-318 for U.S. Patent No. 6,182,184 dated October 4, 2006.	<input type="checkbox"/>	<input type="checkbox"/>
	270.	Response of Hitachi, Ltd. to the Complaint and Notice of Investigation, dated May 19, 2000.	<input type="checkbox"/>	<input type="checkbox"/>
	271.	Sakai, et al., "A Synchronous DRAM with New High-Speed I/O Lines Method for the Multimedia Age," IEICE Trans Electron, Vol. E78-C, No.7, July 1995	<input type="checkbox"/>	<input type="checkbox"/>
	272.	Samsung Claim Chart for Claims 1, 2, 4-7, 11-13, 15-21, 24-31, 33-36, and 38-39 of U.S. Patent No. 6,184,120 based on Park, with additional references to the JEDEC Standard as cited and filed in an Information Disclosure Statement Under 37 CFR §§ 1.933 & 1.555 in <i>Inter Partes</i> Reexaminations of 6,426,916 (95/000,166), 6,324,129 (95/000,178) and 6,182,184 (95/000,183) dated April 9, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	273.	Samsung Claim Chart for Claims 1, 2, 4-7, 11-13, 15-21, 24-31, 33-36, and 38-39 of U.S. Patent No. 6,184,120 based on the JEDEC Standard as cited and filed in an Information Disclosure Statement Under 37 CFR §§ 1.933 & 1.555 in <i>Inter Partes</i> Reexaminations of 6,426,916 (95/000,166), 6,324,129 (95/000,178) and 6,182,184 (95/000,183) dated April 9, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	274.	Samsung Claim Chart for Claims 1-29 of U.S. Patent No. 6,182,184 based on Budde, with additional reference to Inagaki, Johnson, Lofgren, and Grover as cited and filed in an Information Disclosure Statement Under 37 CFR §§ 1.933 & 1.555 in <i>Inter Partes</i> Reexaminations of 6,426,916 (95/000,166), 6,324,129 (95/000,178) and 6,182,184 (95/000,183) dated April 9, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	275.	Samsung Claim Chart for Claims 1-39 of U.S. Patent No. 6,184,120 based on the iAPX Manual, with additional reference to Rau, Yoshida, Olson, Johnson, Lofgren, and Grover as cited and filed in an Information Disclosure Statement Under 37 CFR §§ 1.933 & 1.555 in <i>Inter Partes</i> Reexaminations of 6,426,916 (95/000,166), 6,324,129 (95/000,178) and 6,182,184 (95/000,183) dated April 9, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	276.	Samsung Claim Chart for Claims 1-39 of U.S. Patent No. 6,184,120 based on Budde, with additional reference to Rau, Yoshida, Olson, Johnson, Lofgren, and Grover as cited and filed in an Information Disclosure Statement Under 37 CFR §§ 1.933 & 1.555 in <i>Inter Partes</i> Reexaminations of 6,426,916 (95/000,166), 6,324,129 (95/000,178) and 6,182,184 (95/000,183) dated April 9, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	277.	Samsung Claim Chart for Claims 1-4, 10-17, 20-22, 24-26, and 29 of U.S. Patent No. 6,182,184 based on Hynix-1, with additional references to Hynix-2 and Hynix-3 as cited and filed in an Information Disclosure Statement Under 37 CFR §§ 1.933 & 1.555 in <i>Inter Partes</i> Reexaminations of 6,426,916 (95/000,166), 6,324,129 (95/000,178) and 6,182,184 (95/000,183) dated April 9, 2007.	<input type="checkbox"/>	<input type="checkbox"/>

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			YES	NO
	278.	Samsung Claim Chart for Claims 1-4, 7, 9-17, 19-26, and 28-29 of U.S. Patent No. 6,182,184 based on the JEDEC Standard as cited and filed in an Information Disclosure Statement Under 37 CFR §§ 1.933 & 1.555 in Inter Partes Reexaminations of 6,426,916 (95/000,166), 6,324,129 (95/000,178) and 6,182,184 (95/000,183) dated April 9, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	279.	Samsung Claim Chart for Claims 1-4, 7, 9-17, 19-26, and 28-29 of U.S. Patent No. 6,182,184 based on Park, with additional references to the JEDEC Standard as cited and filed in an Information Disclosure Statement Under 37 CFR §§ 1.933 & 1.555 in Inter Partes Reexaminations of 6,426,916 (95/000,166), 6,324,129 (95/000,178) and 6,182,184 (95/000,183) dated April 9, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	280.	Samsung Claim Chart or Claims 1-29 of U.S. Patent No. 6,182,184 based on the iAPX Manual, with additional reference to Inagaki, Johnson, Lofgren, and Grover as cited and filed in an Information Disclosure Statement Under 37 CFR §§ 1.933 & 1.555 in Inter Partes Reexaminations of 6,426,916 (95/000,166), 6,324,129 (95/000,178) and 6,182,184 (95/000,183) dated April 9, 2007.	<input type="checkbox"/>	<input type="checkbox"/>
	281.	SLDRAM Consortium, 400 Mb/s/pin SLDRAM, 4M x 18SLDRAM Pipelined, Eight Bank, 2.5v Operation, Rev. 8/27/97.	<input type="checkbox"/>	<input type="checkbox"/>
	282.	SyncLink, "Draft Standard for a High-Speed Memory Interface (SyncLink), Microprocessor and Microcomputer Standards Subcommittee of the IEEE Computer Society, IEEE Copyright 1996.	<input type="checkbox"/>	<input type="checkbox"/>
	283.	Thorp, J.S., et al., "Real Time Voltage-Phasor Measurements for Static State Estimation," IEEE Transactions on Power Apparatus and Systems, Vol. PAS-104, No. 11, pgs 3098-3106, 11 November 1985.	<input type="checkbox"/>	<input type="checkbox"/>
	284.	Vasanthavada, et al., "Synchronization of Fault-Tolerant Clocks in the Presence of Malicious Failures," IEEE Transactions on Computers, Vol. 37, No. 4, pp. 440-449, April 1988	<input type="checkbox"/>	<input type="checkbox"/>
	285.	Yoshikawa, et al., "A Precise - TDMA Synchronization Scheme Employing Onboard Reference Burst Generator," IEEE International Conference on Communications 1985, Vol. 2, pp. 994-998, June 1985	<input type="checkbox"/>	<input type="checkbox"/>
	286.	"Burndy Connect," Advertisement, Electronic Engineering Times, pg. T24-T25, February 24, 1986.	<input type="checkbox"/>	<input type="checkbox"/>
	287.	Transmittal of Office Action Communication in Third Party Requester Inter Partes Reexamination Control No. 95/001,008 (6,715,020) mailed February 15, 2008	<input type="checkbox"/>	<input type="checkbox"/>

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